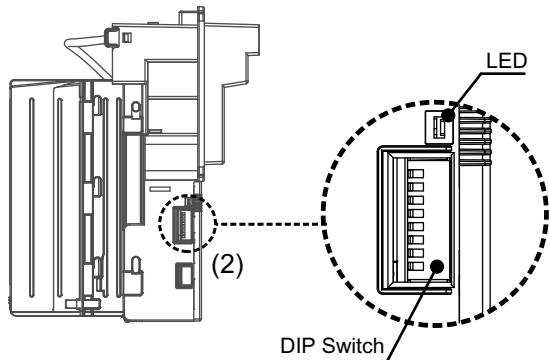


## XBA-RON4 Option Switch Settings

Supported bill ROL 1, 5, 10, 50 4bills.

FUNCTION	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8
★ Reject RON 1	ON							
★ Accept RON 1	OFF							
★ Reject RON 5		ON						
★ Accept RON 5		OFF						
★ Reject RON 10			ON					
★ Accept RON 10			OFF					
★ Reject RON 50				ON				
★ Accept RON 50				OFF				
★ Reserved					ON			
★ Reserved					OFF			
★ Disable Bill Reject 4 Times BA Stop By 30 Sec						ON		
★ Enable Bill Reject 4 Times BA Stop By 30 Sec						OFF		
★ Stack Banknote when Power-up							ON	
★ Reject Banknote when Power-up							OFF	
★ ENABLE IrDA Download								ON
★ Reserved								OFF

This dip switch is located at the side of XBA.



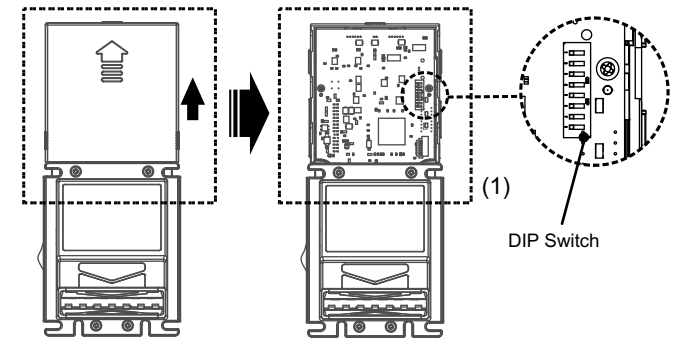
## Currency Assign Data

Interface Bill value	ICT002U	Parallel A3	ICT104V	Pulse	MDB
BV1	RON 1	RON 1	RON 1	RON 1	RON 1
BV2	RON 5	RON 5	RON 5	RON 5	RON 5
BV3	RON 10	RON 10	RON 10	RON 10	RON 10
BV4	RON 50	RON 50	RON 50	RON 50	RON 50

## XBA-RON4 (Pulse/MDB/ICT/Parallel A3)

FUNCTION	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8
Inhibit Active HIGH	ON							
★ Inhibit Active LOW	OFF							
★ ENABLE INHIBIT-Sleep only		ON						
★ 5 second Timeout only		OFF						
★ Normal (Note.2)			OFF	OFF				
★ Mode 1 (Note.3)			OFF	ON				
★ Mode 2 (Note.4)			ON	OFF				
★ Mode 1 + 2 (Note.5)			ON	ON				
★ Credit-Pulse Normal HIGH					ON			
★ Credit-Pulse Normal LOW					OFF			
★ Pulse Mode						ON		
★ Other Mode						OFF		
<b>Pulse Mode</b>						<b>Other Mode</b>		
★ 1 pulse / RON 1							MDB Mode	OFF OFF
★ 2 pulse / RON 1							ICT002U Mode	OFF ON
★ 10 pulse / RON 1							Parallel A3 Mode	ON OFF
★ 20 pulse / RON 1							ICT104V Mode	ON ON

This dip switch is located on the CPU board, remove the CPU board cover first.



### ★ Manufacture setting

- Note :
1. After setting dip switch of the credit pulses, you should reset the bill acceptor again.
  2. Normal : No sleep function.
  3. Mode 1 : Bill-acceptor wakes up VMC with banknote only (special cable required).
  4. Mode 2 : VMC wakes up bill-acceptor.
  5. Mode 1+2 : Bill-acceptor wakes up with banknote or by VMC.