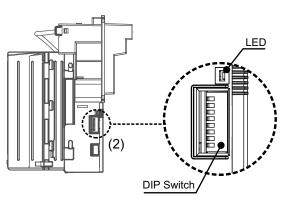
# XBA-JPY4 Option Switch Settings Supported bill ¥ 1000, 2000, 5000, 10000 4bills.

ı									
	FUNCTION	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8
	Reject ¥ 1000	ON							
*	Accept ¥ 1000	OFF							
	Reject ¥ 2000		ON						
*	Accept ¥ 2000		OFF						
	Reject ¥ 5000			ON					
*	Accept ¥ 5000			OFF					
	Reject ¥ 10000				ON				
*	Accept ¥ 10000				OFF				
	Reserved					ON			
*	Reserved					OFF			
	Disable Bill Reject 4 Times BA Stop By 30 Sec						ON		
*	Enable Bill Reject 4 Times BA Stop By 30 Sec						OFF		
	Stack Banknote when Power-up							ON	
*	Reject Banknote when Power-up							OFF	
	ENABLE IrDA Download								ON
*	Reserved								OFF

This dip switch is located at the side of XBA.



#### **Currency Assign Data**

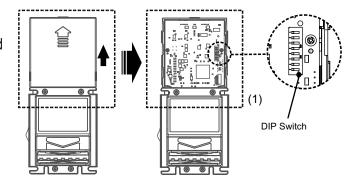
Interface Bill value	ICT002U	Parallel A3	ICT104V	Pulse	MDB
BV1	¥ 1000	¥ 1000	¥ 1000	¥ 1000	¥ 1000
BV2	¥ 2000	¥ 2000	¥ 2000	¥ 2000	¥ 2000
BV3	¥ 5000	¥ 5000	¥ 5000	¥ 5000	¥ 5000
BV4	¥ 10000	¥ 10000	¥ 10000	¥ 10000	¥ 10000

## \_Appendix

## XBA-JPY4 (Pulse/MDB/ICT/Parallel A3)

FUNCTION	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8
Inhibit Active HIGH	ON							
Inhibit Active LOW	OFF							
ENABLE INHIBIT-Sleep only		ON						
5 second Timeout only		OFF						
Normal (Note.2)			OFF	OFF				
Mode 1 (Note.3)			OFF	ON				
Mode 2 (Note.4)			ON	OFF				
Mode 1 + 2 (Note.5)			ON	ON				
Credit-Pulse Normal HIGH					ON			
Credit-Pulse Normal LOW					OFF			
Pulse Mode						ON		
Other Mode						OFF		
Pulse Mode	Other Mode							
1 pulse / ¥ 1000	MDB Mode					OFF	OFF	
5 pulse / ¥ 1000	ICT002U Mode				OFF	ON		
10 pulse / ¥ 1000	Parallel A3 Mode					ON	OFF	
25 pulse / ¥ 1000	ICT104V Mode						ON	ON
	Inhibit Active HIGH Inhibit Active LOW ENABLE INHIBIT-Sleep only 5 second Timeout only Normal (Note.2) Mode 1 (Note.3) Mode 2 (Note.4) Mode 1 + 2 (Note.5) Credit-Pulse Normal HIGH Credit-Pulse Normal LOW Pulse Mode Other Mode Pulse Mode 1 pulse / ¥ 1000 5 pulse / ¥ 1000	Inhibit Active HIGH Inhibit Active LOW OFF ENABLE INHIBIT-Sleep only 5 second Timeout only Normal (Note.2) Mode 1 (Note.3) Mode 2 (Note.4) Mode 1 + 2 (Note.5) Credit-Pulse Normal HIGH Credit-Pulse Normal LOW Pulse Mode Other Mode Pulse Mode 1 pulse / ¥ 1000 5 pulse / ¥ 1000	Inhibit Active HIGH Inhibit Active LOW Inhibit Active LOW OFF  ENABLE INHIBIT-Sleep only 5 second Timeout only Normal (Note.2) Mode 1 (Note.3) Mode 2 (Note.4) Mode 1 + 2 (Note.5) Credit-Pulse Normal HIGH Credit-Pulse Normal LOW Pulse Mode Other Mode Pulse Mode 1 pulse / ¥ 1000 5 pulse / ¥ 1000 10 pulse / ¥ 1000	Inhibit Active HIGH				

This dip switch is located on the CPU board, remove the CPU board cover first.



#### ★ Manufacture setting

Note: 1.Affer setting dip switch of the credit pulses, you should reset the bill acceptor again.

2.Normal : No sleep function.

3.Mode 1 : Bill-acceptor wakes up VMC with banknote only (special cable required).

4.Mode 2 : VMC wakes up bill-acceptor.

5.Mode 1+2: Bill-acceptor wakes up with banknote or by VMC.

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